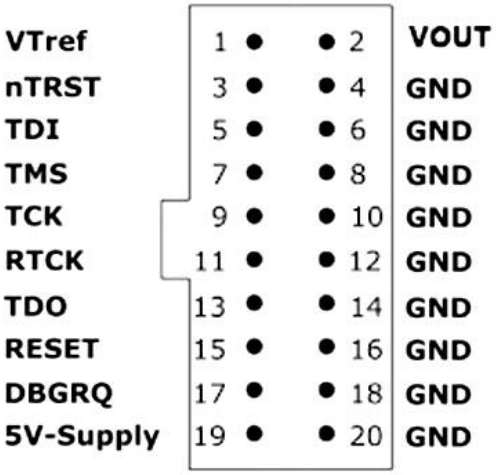
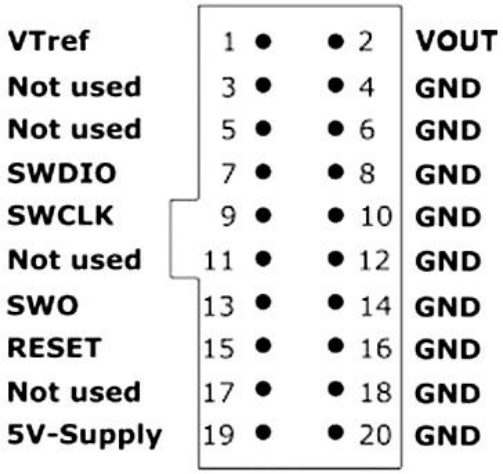
JLINK V8 Debugger J-LINK Arm Cortex-M4/M0 Emulation Downloader User's Manual

Interface specification

JTAG interface



SWD interface



5V-Supply pin can control the output with instructions as follows:

Command	Explanation
Power On	Switch Target Power On(Put through power supply)
Power Off	Switch Target Power Off(Shut off the power)
Power On Perm	Set Target Power Supply Default To "On"(Power on by default)
Power Off Perm	Set Target Power Supply Default To "Off"(Power off by default)

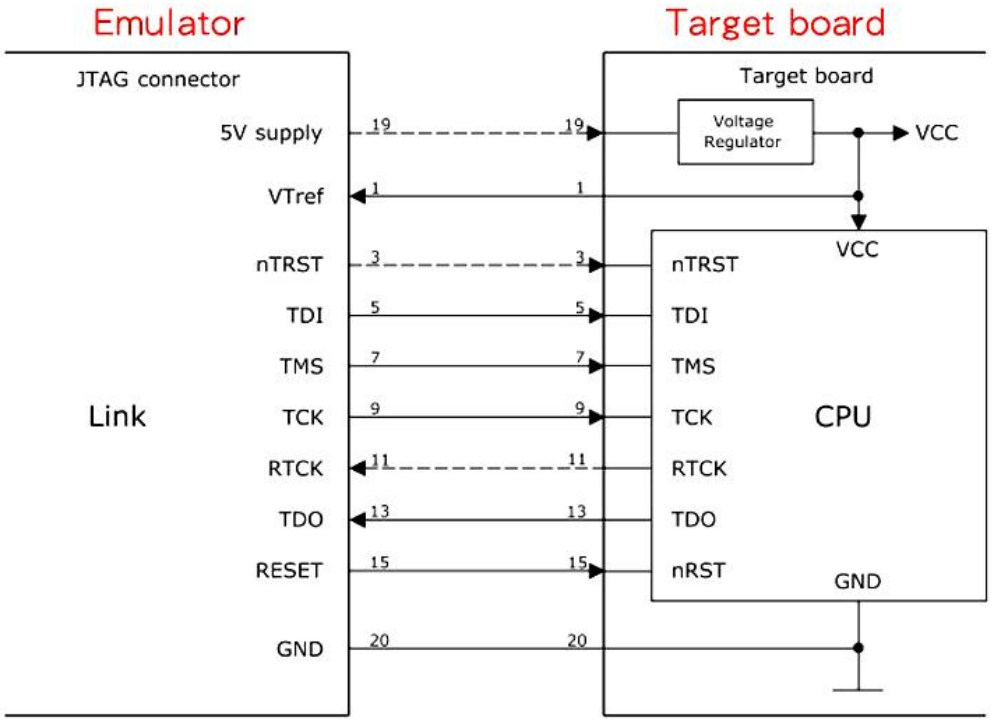
VOUT is a 3.3V voltage output pin.

The user can remove the short circuit cap inside the rear device and turn off the 3.3V output.

VTREF is the internal voltage reference and this pin must be connected to the V CC of the MCU.

Instructions

JTAG connection mode

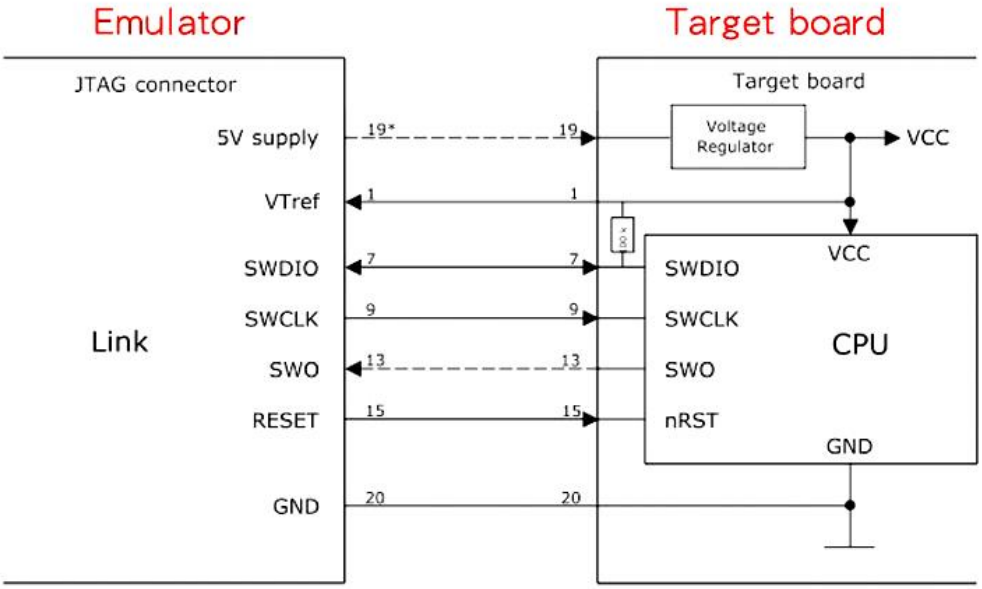


There are generally three kinds of power supply:

1. Use 5V-Supply pin to power MCU through LDO of target board.
2. Connect VOUT pin directly to VCC of MCU for power supply.
3. the user can also choose the target board with power supply.

Note: Regardless of the power supply, the VTREF pin must be connected to the VCC of the MCU.

JTAG connection mode



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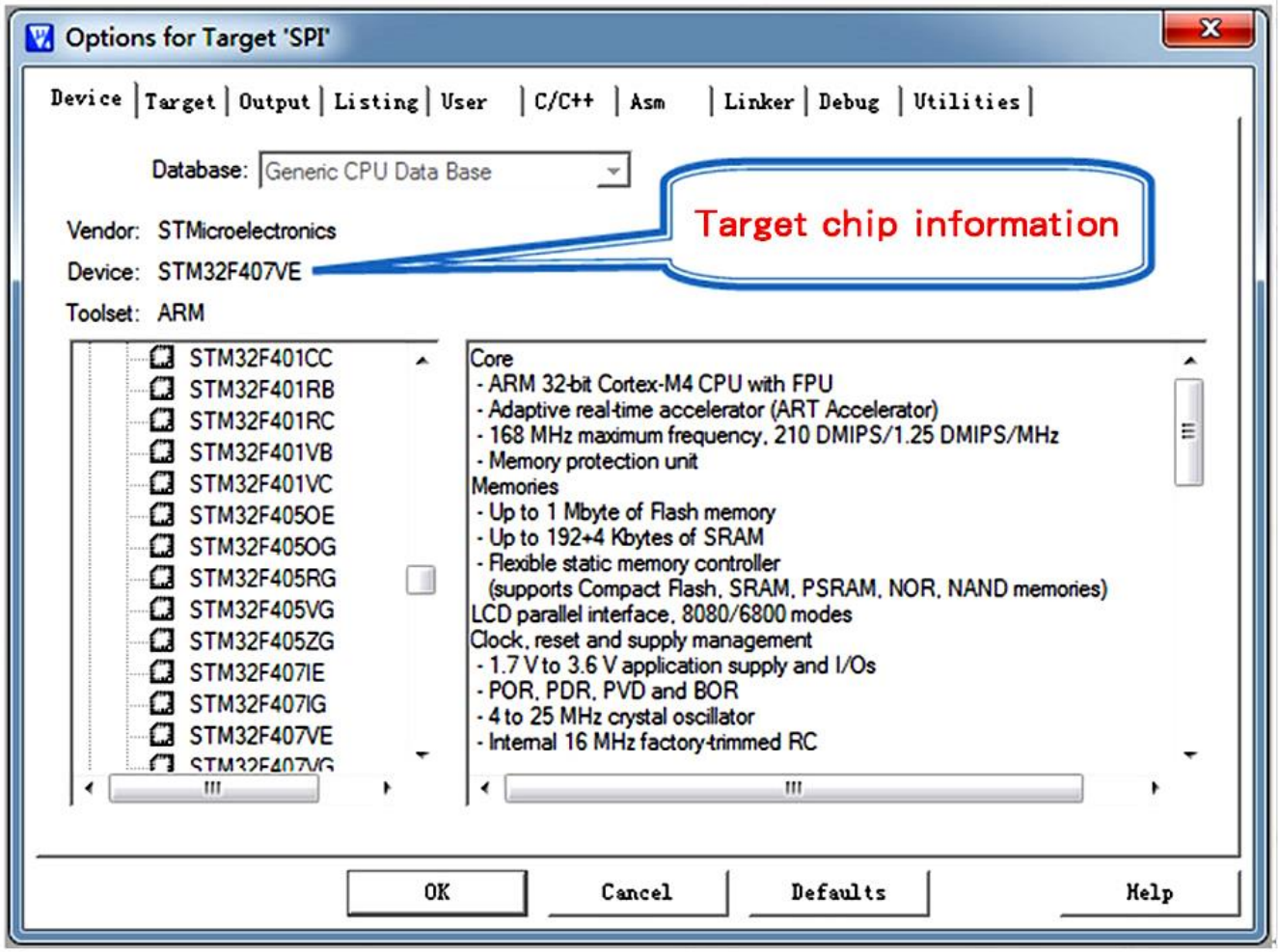
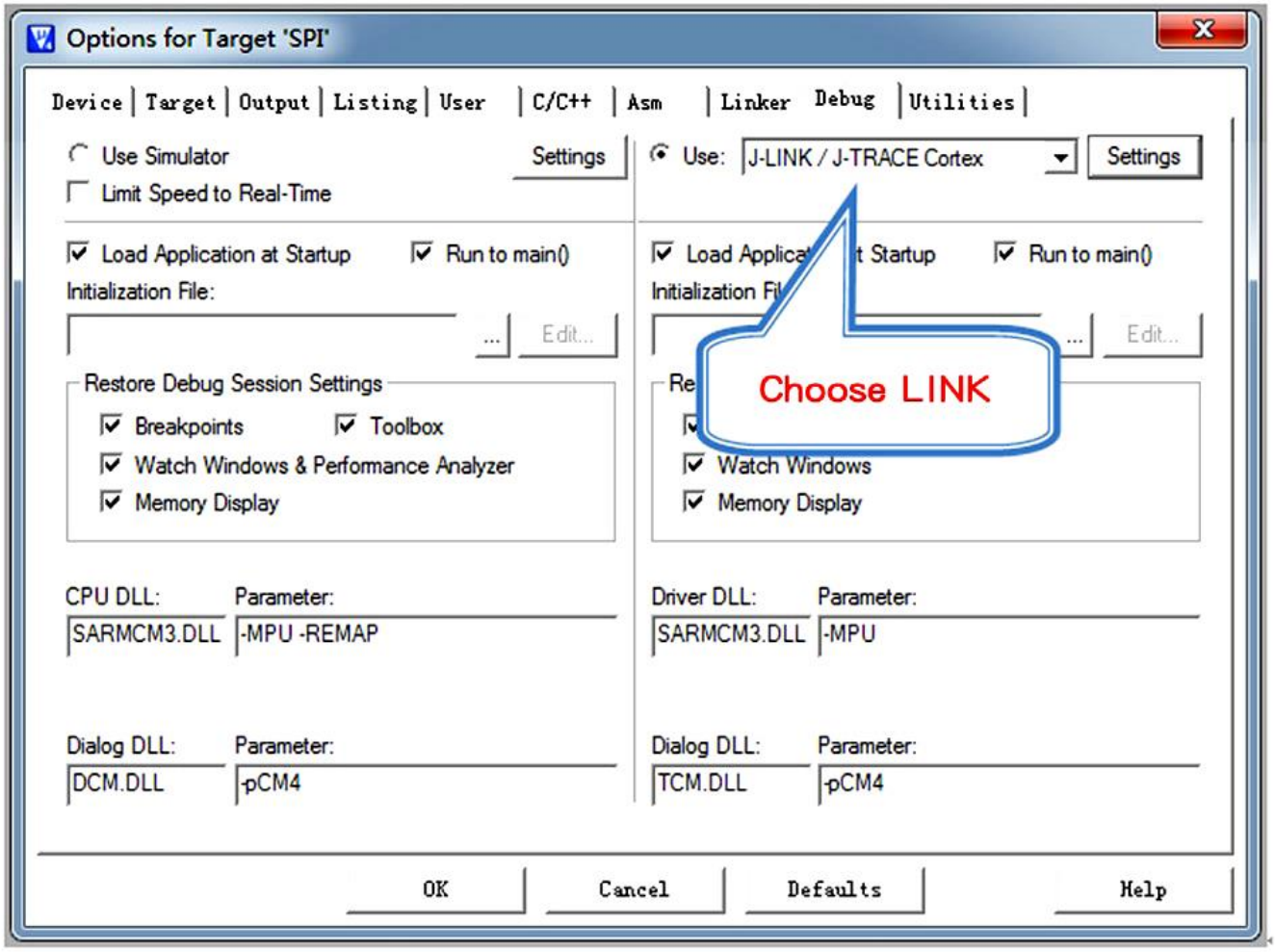
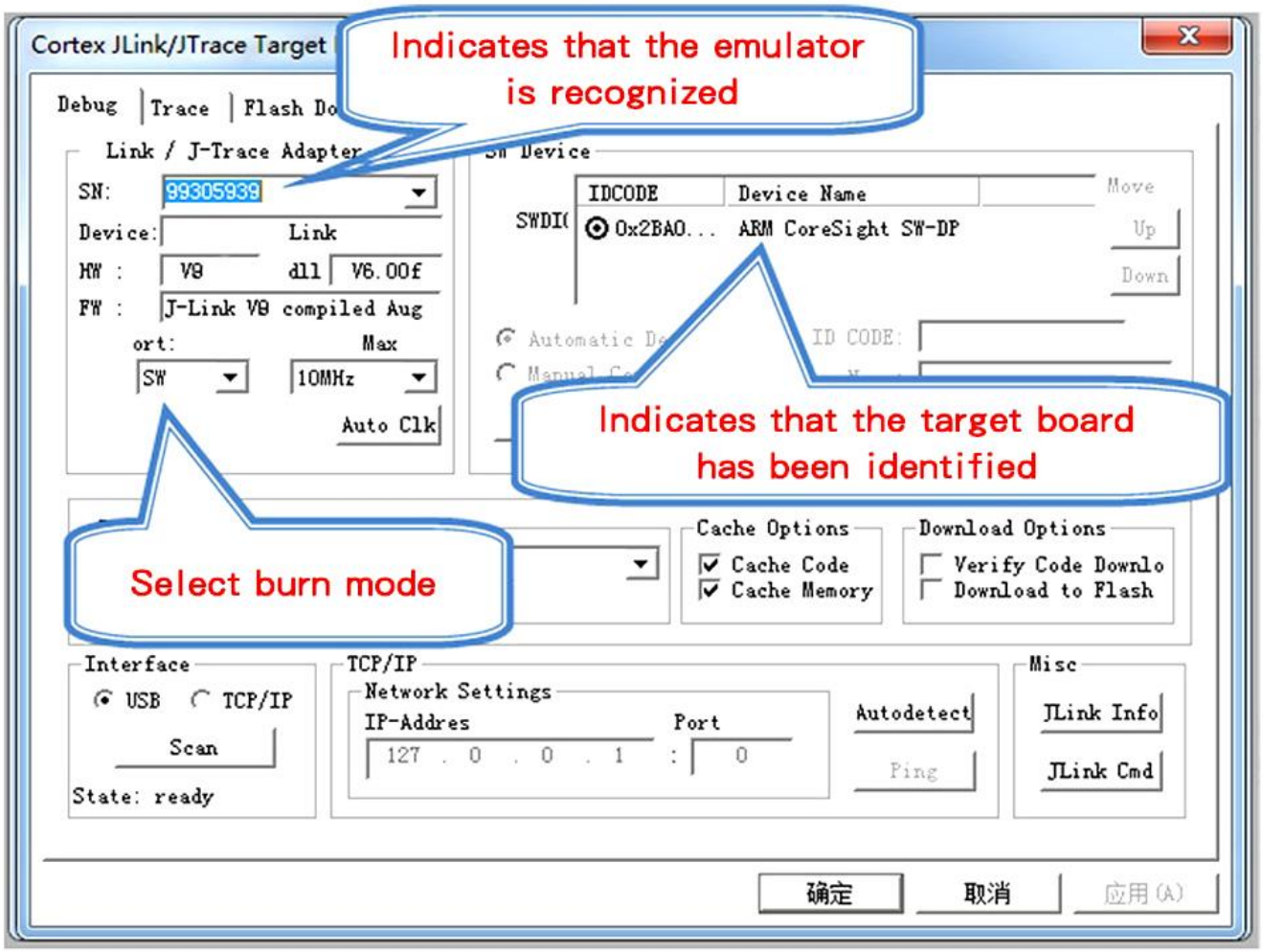
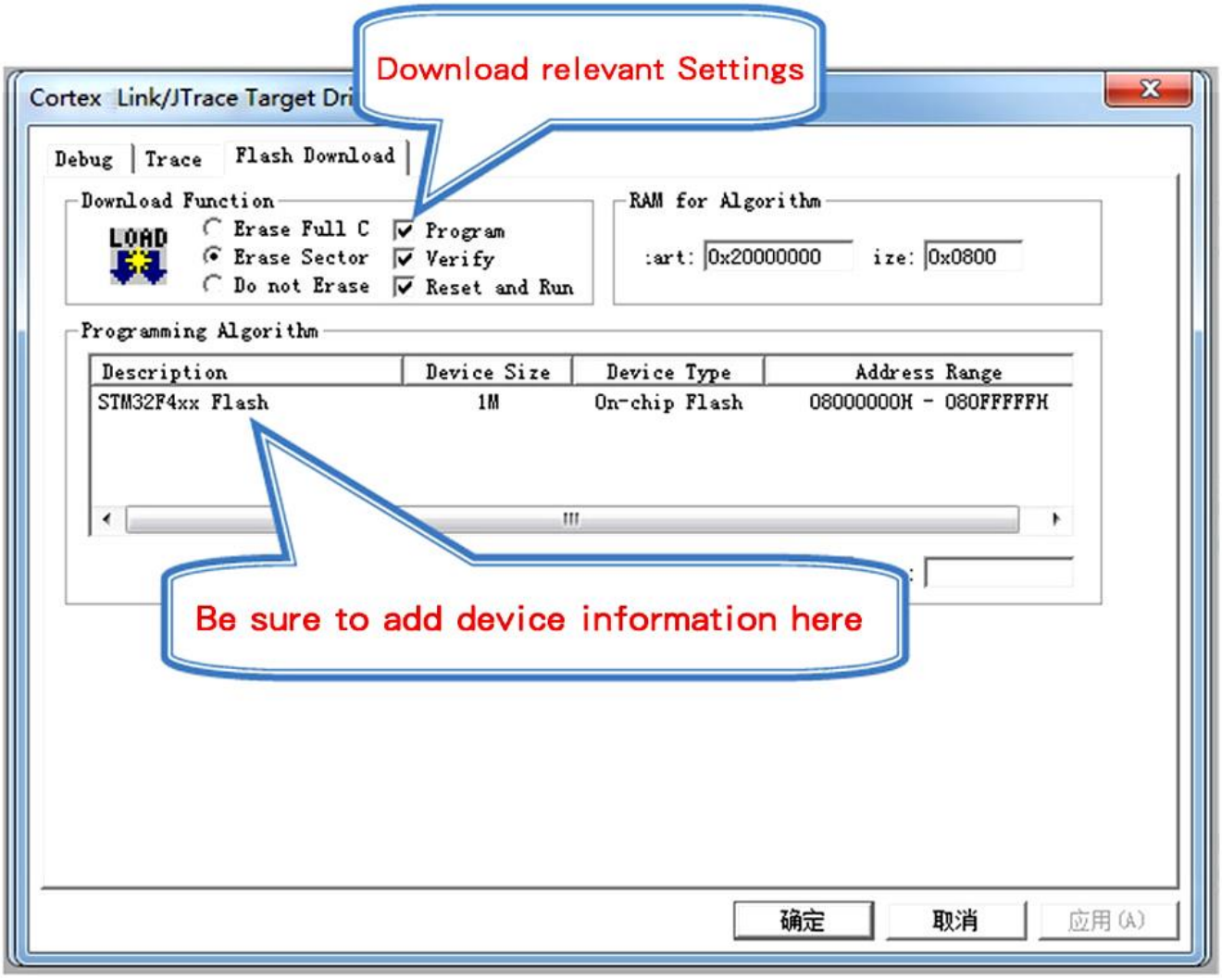
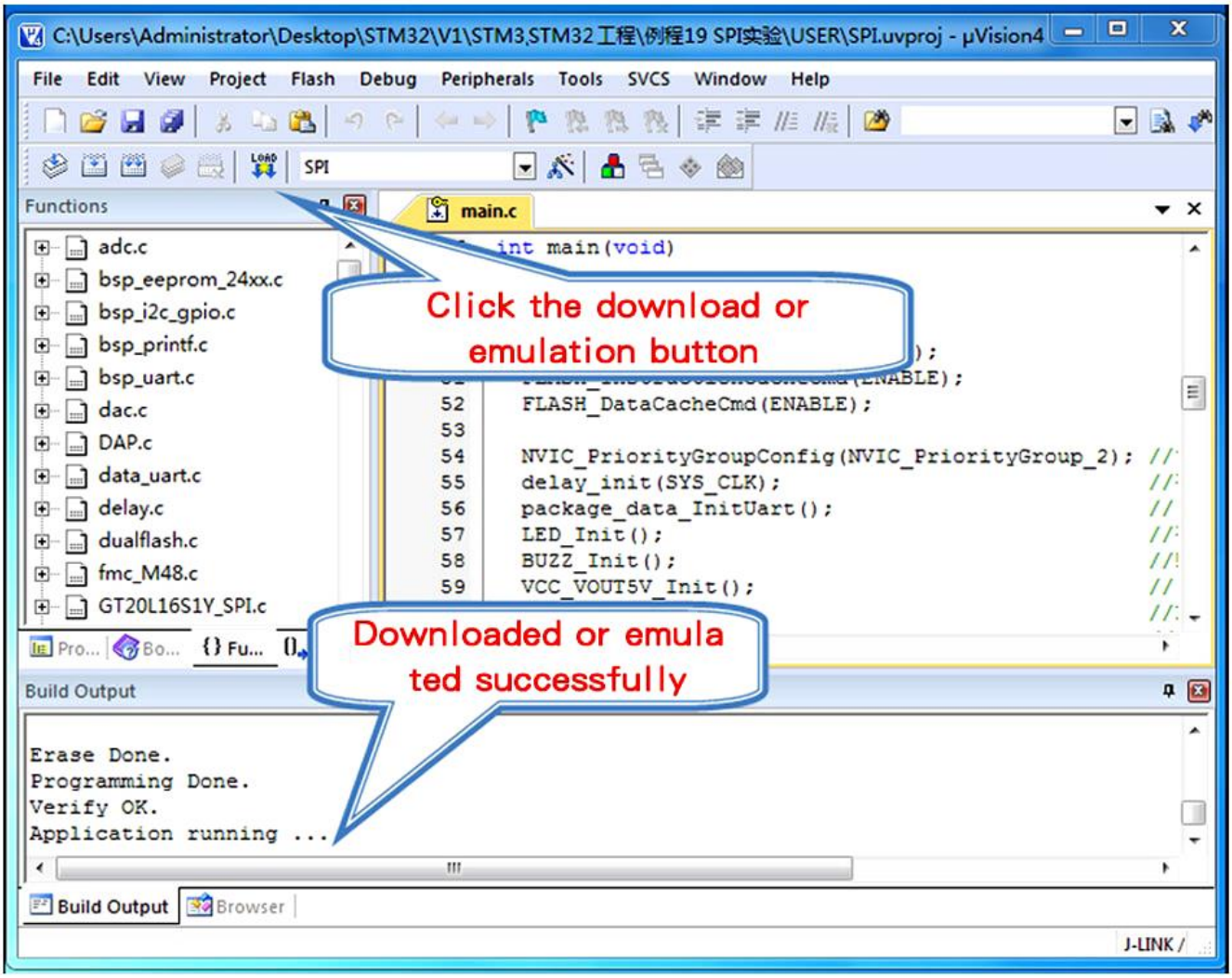



Options for Target 'SPI'
Device | Target | Output | Listing | User | C/C++ | Asm | Linker | Debug | Utilities
Use Simulator
Limit Speed to Real-Time
Settings
Use: J-LINK / J-TRACE Cortex
Settings
Load Application at Startup
Run to main()
Initialization File:
Edit...
Restore Debug Session Settings
Breakpoints
Toolbox
Watch Windows & Performance Analyzer
Memory Display
Choose LINK
Watch Windows
Memory Display
CPU DLL:
Parameter:
SARMCM3.DLL
-MPU -REMAP
Driver DLL:
Parameter:
SARMCM3.DLL
-MPU
Dialog DLL:
Parameter:
DCM.DLL
-pCM4
Dialog DLL:
Parameter:
TCM.DLL
-pCM4
OK
Cancel
Defaults
Help


Cortex JLink/JTrace Target
Indicates that the emulator is recognized
Debug
Trace
Flash Do
Link / J-Trace Adapter
SN:
99305939
Device:
Link
HW :
V8
dll
V6.00f
FW :
J-Link V8 compiled Aug
ort:
Max
SW
10MHz
Auto Clk
SW Device
IDCODE
Device Name
Move
SWDI(
0x2BA0...
ARM CoreSight SW-DP
Up
Down
Automatic De
ID CODE:
Manual Co
Indicates that the target board has been identified
Select burn mode
Cache Options
Cache Code
Cache Memory
Download Options
Verify Code Downlo
Download to Flash
Interface
USB
TCP/IP
Scan
State: ready
TCP/IP
Network Settings
IP-Addres
Port
127 . 0 . 0 . 1 : 0
Autodetect
Ping
Misc
JLink Info
JLink Cmd
确定
取消
应用(A)


Download relevant Settings
Cortex Link/JTrace Target Dri
Debug
Trace
Flash Download
Download Function
LOAD
Erase Full C
Erase Sector
Do not Erase
Program
Verify
Reset and Run
RAM for Algorithm
:art: 0x20000000
ize: 0x0800
Programming Algorithm
Description
Device Size
Device Type
Address Range
STM32F4xx Flash
1M
On-chip Flash
08000000H - 080FFFFFH
Be sure to add device information here
确定
取消
应用(A)


C:\Users\Administrator\Desktop\STM32\V1\STM3,STM32 工程\例程19 SPI实验\USER\SPI.uvproj - µVision4
File Edit View Project Flash Debug Peripherals Tools SVCS Window Help
SPI
Functions
adc.c
bsp_eeprom_24xx.c
bsp_i2c_gpio.c
bsp_printf.c
bsp_uart.c
dac.c
DAP.c
data_uart.c
delay.c
dualflash.c
fmc_M48.c
GT20L16S1Y_SPI.c
main.c
int main(void)
52 FLASH_DataCacheCmd(ENABLE);
53
54 NVIC_PriorityGroupConfig(NVIC_PriorityGroup_2); //
55 delay_init(SYS_CLK); //
56 package_data_InitUart(); //
57 LED_Init(); //
58 BUZZ_Init(); //
59 VCC_VOUT5V_Init(); //
Click the download or emulation button
Downloaded or emula ted successfully
Build Output
Erase Done.
Programming Done.
Verify OK.
Application running ...
Build Output
Browser
J-LINK /