JLINK V8 Debugger J-LINK Arm Cortex-M4/M0 Emulation Downloader

Description:

J-Link V8 is a JTAG emulator that supports emulation of ARM core chips. With IAR EWARM, ADS, WINARM, RealView MDK and other integrated development environments, it supports the simulation of all ARM7, ARM9, Cortex-M3 core chips. Learn to develop the best and most practical development tools for ARM.

(1) Use the original firmware program of JLINK_V8, support automatic upgrade, support KEIL5.

(2) Refer to the circuit principle of the original JLINK_V8.

(3) Refer to the circuit layout of the original JLINK_V8.

(4) Refer to the line routing of the original JLINK_V8.

(5) The power supply circuit uses a large-capacity non-polar capacitor as a filter, which is the same as the original JLINK_V8.

(6) The shell of the USB socket is grounded in parallel through a resistor and a capacitor, which is the same as the original JLINK_V8.

(7) Use a red and green two-color LED to indicate the internal working status (green light indicates the working status, red light indicates the reset status), which is the same as the original JLINK_V8.

(8) Both SWD and JTAG support 1.2-5V voltage, the same as the original JLINK_V8.

(9) Double protection: JLinkUSB port esd protection ic full protection.

(10) JLINK_V8 adopts 1.5A high current 8550 transistor to ensure that the power on command can output 400ma current! .

(11) Machine patch production, strict quality inspection, so that the quality of the product is 100% qualified, please use it with confidence.

Functions and Features:

- USB2.0 interface
- ARM7/9/11, Cortex-A5/A8/A9, Cortex-M0/M1/M3/M4, Cortex-R4 core
- Serial Wire Debug (SWD)
- Serial Wire Viewer (SWV)
- Download speed up to 720kb/s
- DCC mode up to 800kb/s
- Seamless integration with IAR Workbench, Keil, ADS, RVDS
- No need for external power supply, take power through USB
- Maximum JTAG speed is 12MHz
- Automatically identify the kernel
- Recognition of speed
- Support adaptive clock

- Can monitor all JTAG signals and automatically adapt to the target board voltage
- Multi-core debugging
- Plug and play
- Includes 20-pin standard JTAG connector
- 1.2V~5V target board voltage
- Includes USB and 20-pin flat cable
- Future cache tracking Embedded Trace Buffer (ETB)
- Optional online programming, programming FLASH software
- RDI plug-in makes J-Link suitable for any RDI compatible debugger such as ADS, Relview, IAR, etc.
- J-Link TCP/IP server, allowing to use J-Link over TCP/IP network

Supported Operating Systems:
- Microsoft Windows XP
- Windows 7
- Windows 7 x64

Supported Software:
- Directly support ADS, IAR, KEIL, WINARM, REALVIEW and other development environments

Supported Devices:
- all ARM7 and ARM9 series embedded microcontrollers with JTAG interface
- all Cortex-M series embedded microcontrollers with JTAG/SWD interface

The Firmware Can Be Upgraded:
In the future, ARM7, ARM9, Cortex-M series microcontrollers will launch more models, and new device models will be added to the device support list. When you need to use the latest models in future development, just upgrade to the SEGGER website to download. The latest version of the firmware, the emulator will automatically upgrade the firmware program after use, so that the new model can be supported

J-Link Support for ARM Cores:
- ARM7TDMI (Rev 1)
- ARM7TDMI (Rev 3)
- ARM7TDMI-S (Rev 4)
- ARM720T * ARM920T
- ARM926EJ-S
- ARM946E-S
- ARM966E-S
- ARM11
- Architecture (M3

Product size

53mm   102mm

Made in China