IC Card 24C02

Overview:

- 24C02 is a 2K bit Serial CMOS E2PROM
- Internally contains 256 8-bit bytes
- CATALYST's advanced CMOS technology substantially reduces device power consumption.
- 24C02 has a 16-byte page write buffer.
- The device is operated via IIC bus interface.

Specifications:

- Operating temperature: -55 ℃ +125 ℃ industrial grade
- Commercial Grade: 0 ℃ +75 ℃
- Storage temperature: -65 ℃ +150 ℃
- Each pin withstand voltage: -2.0 Vcc +2.0 V
- Vcc pin withstand voltage: -2.0 +7.0 V
- Package power dissipation (Ta = 25 ℃): 1.0W
- Soldering Temperature (10 seconds): 300 ℃
- Output short-circuit current: 100mA

Functional Description:

- 24C02 support IC, bus data transfer protocol IC, bus protocol provides that any data sent to the device on the bus as a transmitter. Any of the receiver devices receives data from the bus.
- Data transfer is generated by the serial clock and all start stop signal master control.
- Master and slave devices can be used as transmitter or receiver, but the master device to control data transfer (sending or receving) mode through the device address inputs A0, A1 and A2 can be up to eight 24C02 devices connected to the bus on.

Pin descriptions:

Table 2 Pin description:

Pin Name	Function
A0 A1 A2	Select the device address
SDA	Serial Data / Address
SCL	Serial Clock
WP	Write Protect
Vcc	+1.8 V ~ 6.0V operating voltage
Vss	Ground

SCL Serial Clock:

24C02 serial clock input pin used to produce devices of all data sent or received clock, which is an input pin.

A0, A1, A2 device address inputs:

These set the device address input pin for cascading multiple devices, the default value when these feet dangling 0.

When using 24C02 maximum eight devices can be cascaded. If only one bus 24C02 is addressed, the three address input pin (A0, A1, A2) can be left floating or connected to Vss, if there is only one bus 24C02 is addressing these three address input pins (A0, A1, A2) must be connected to Vss.

WP Write Protect:

If the WP pin is connected to Vcc, all content is write-protected can be read. When the WP pin is connected to Vss or floating devices allow normal read / write operations

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