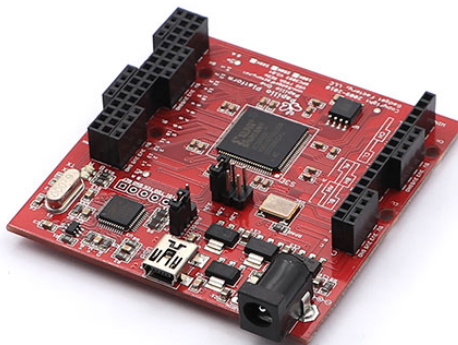


FPGA Papilio One 500K

Model: DEV-11158

User Manual



Papilio Software:

Papilio Arduino IDE	Integrates the Papilio Loader and AVR8 Soft Processor into the Arduino IDE to allow seamless usage of the Arduino IDE with the Papilio One.
Papilio Loader GUI (Info)	Load bitstreams generated by Xilinx ISE to the Papilio One. Version 2.0 as Java GUI Windows version
SUMP Logic Analyzer	Turn the Papilio One into a 32 channel Logic Analyzer.
Papilio Linux Virtual machine from SuseStudio.	Start using the Papilio tools and ZPUino without having to install or configure anything.

Papilio One Technical Docs:

- Papilio UCF Files - Papilio user constraint file (ucf) for use with Xilinx ISE projects.
- Pin Mappings - Papilio chart shows what FPGA pins are connected to Wing locations.
- Papilio Loader Help - Help page for Papilio Loader explains different modes and options.

Soft Processors:

Synthesize your custom AVR8 Soft Processor, then upload sketches with the Arduino IDE.

AVR8 Source Code V1.6

AVR8 Source Code V1.5

ZPUino Soft Processor is a 32 bit 100 MHz processor that is capable of running Arduino Sketches.

Papilio Wing Templates:

Use these EAGLE templates to make your own Wings.

8-bit Wing Template The 8-bit Wing has 8 I/O lines.

16-bit Wing Template The 16-bit Wing has 16 I/O lines.